8-1590 H-17A

Attorney's Docket No. 018414-082

Group Art Unit: 2318

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

William K. ZURAVLEFF et al.

/437,975) Examiner: F. Asta

Application No.: 08/437,975

Filed: May 10, 1995

For: CONTROLLER FOR

SYNCHRONOUS DRAM

RECEIVED

AUG 1 5 1996

GROUP 2300

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

In response to the Office dated May 15, 1996 please amend the above-identified application as follows:

IN THE TITLE:

Please change the title from "A CONTROLLER FOR A SYNCHRONOUS DRAM" to --A CONTROLLER FOR A SYNCHRONOUS DRAM THAT MAXIMIZES THROUGHPUT BY ALLOWING MEMORY REQUESTS AND COMMANDS TO BE ISSUED OUT OF ORDER--.

IN THE ABSTRACT:

At the end of the abstract please add — To achieve this maximized throughput, memory requests are tagged for indicating a sending order. Thereafter, the memory requests may be arbitrated when conflicting memory requests are queued and this arbitration process is then decoded for simultaneously updating scheduling constraints. The memory

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